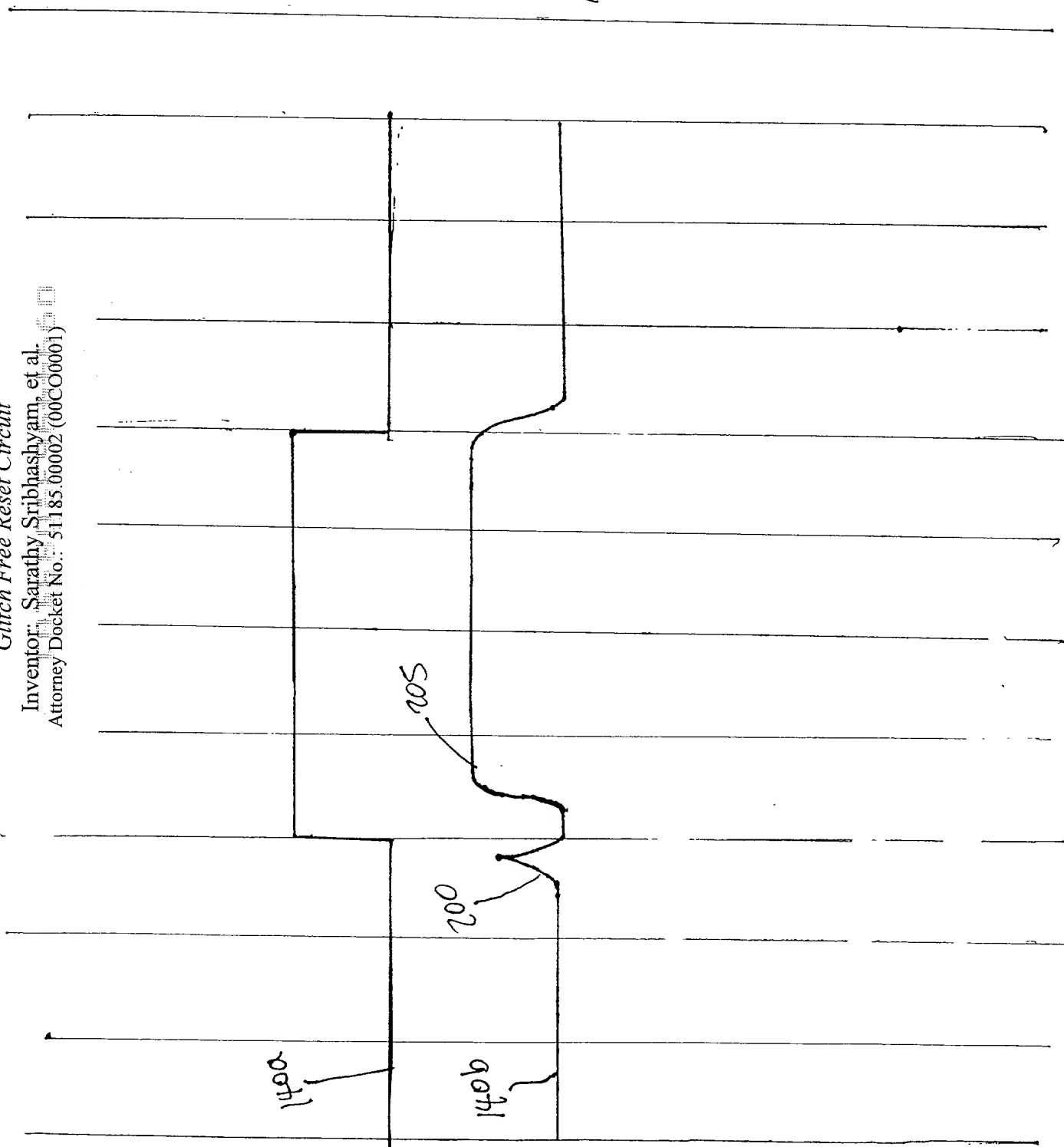


Figure 1
(Prior Art)

Figure 2



300

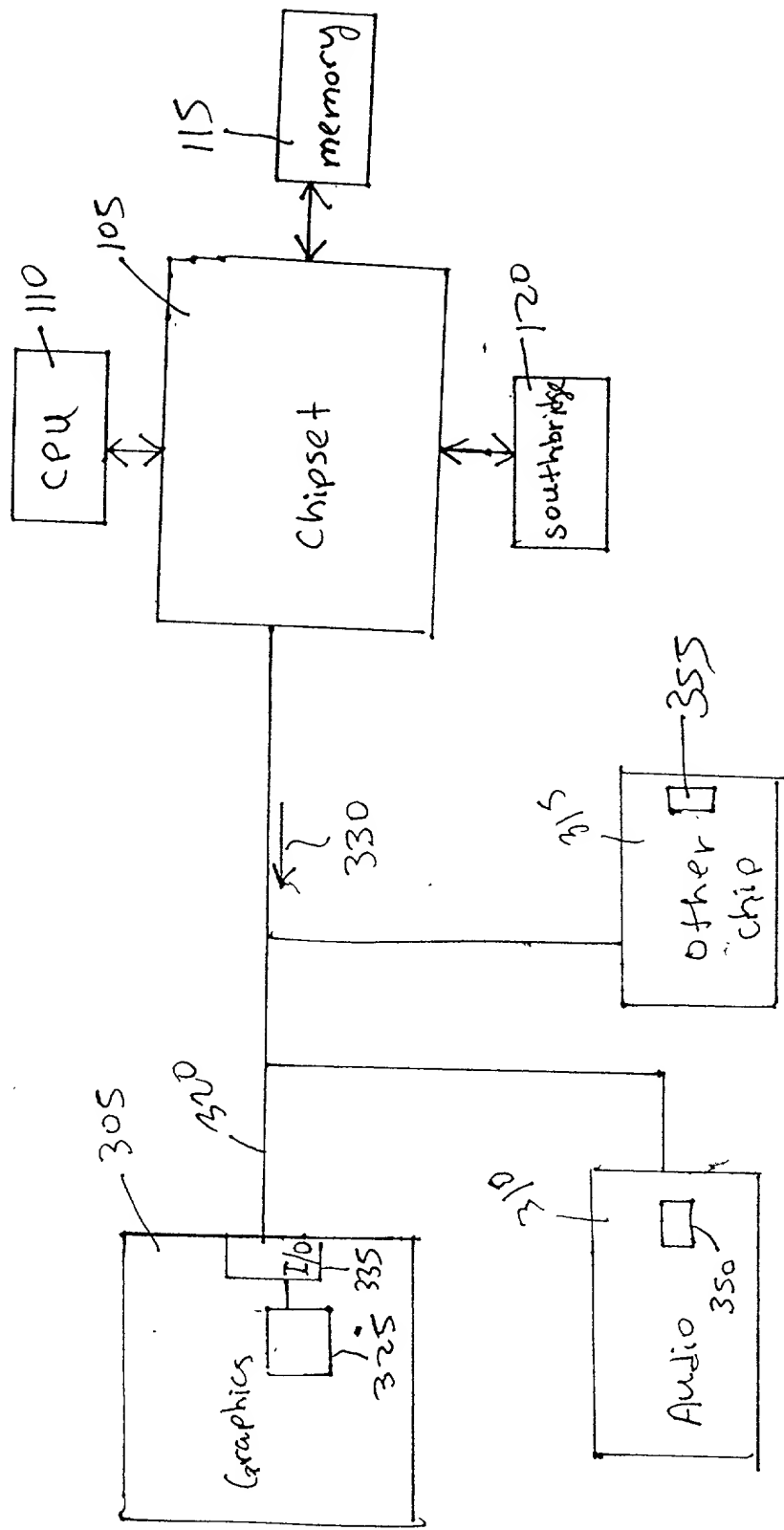
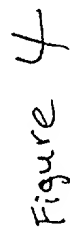


Figure 3



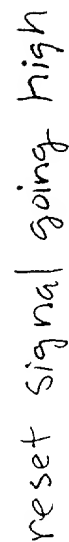


Figure 5

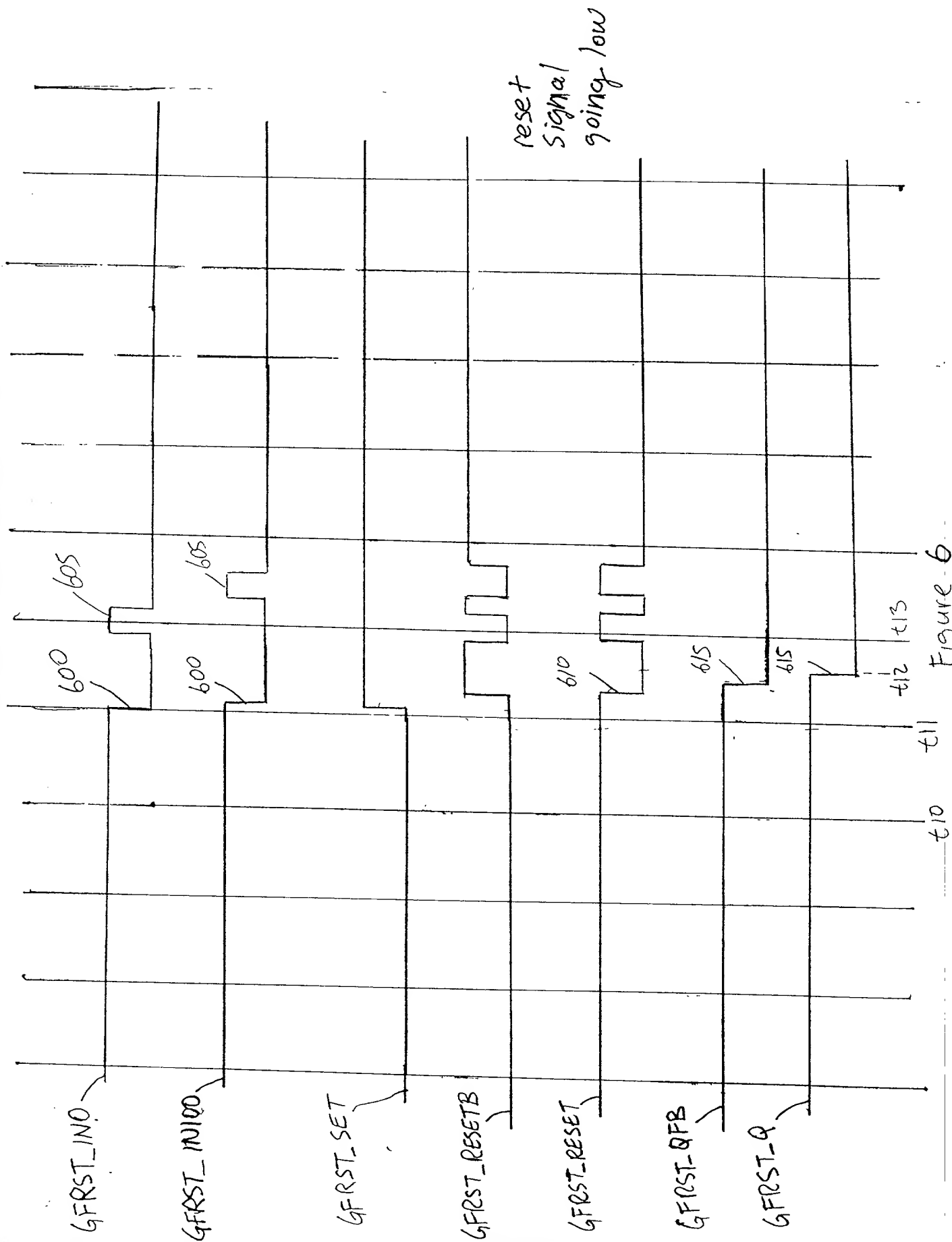


Figure 6

Glitch Free Reset Circuit

Inventor: Sarathy Sribhashyam, et al.
Attorney Docket No.: 51185.00002 (00CO0001)

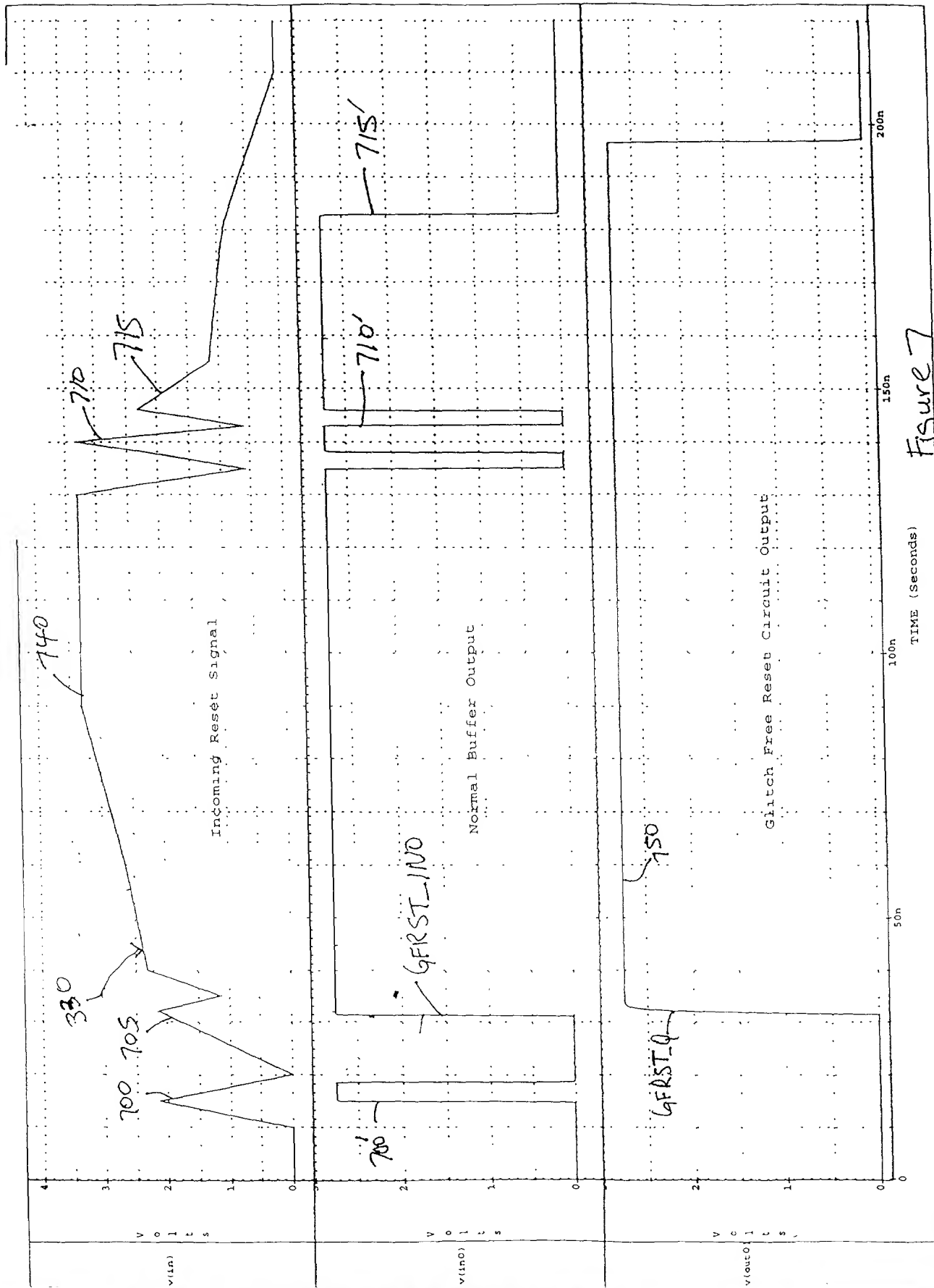
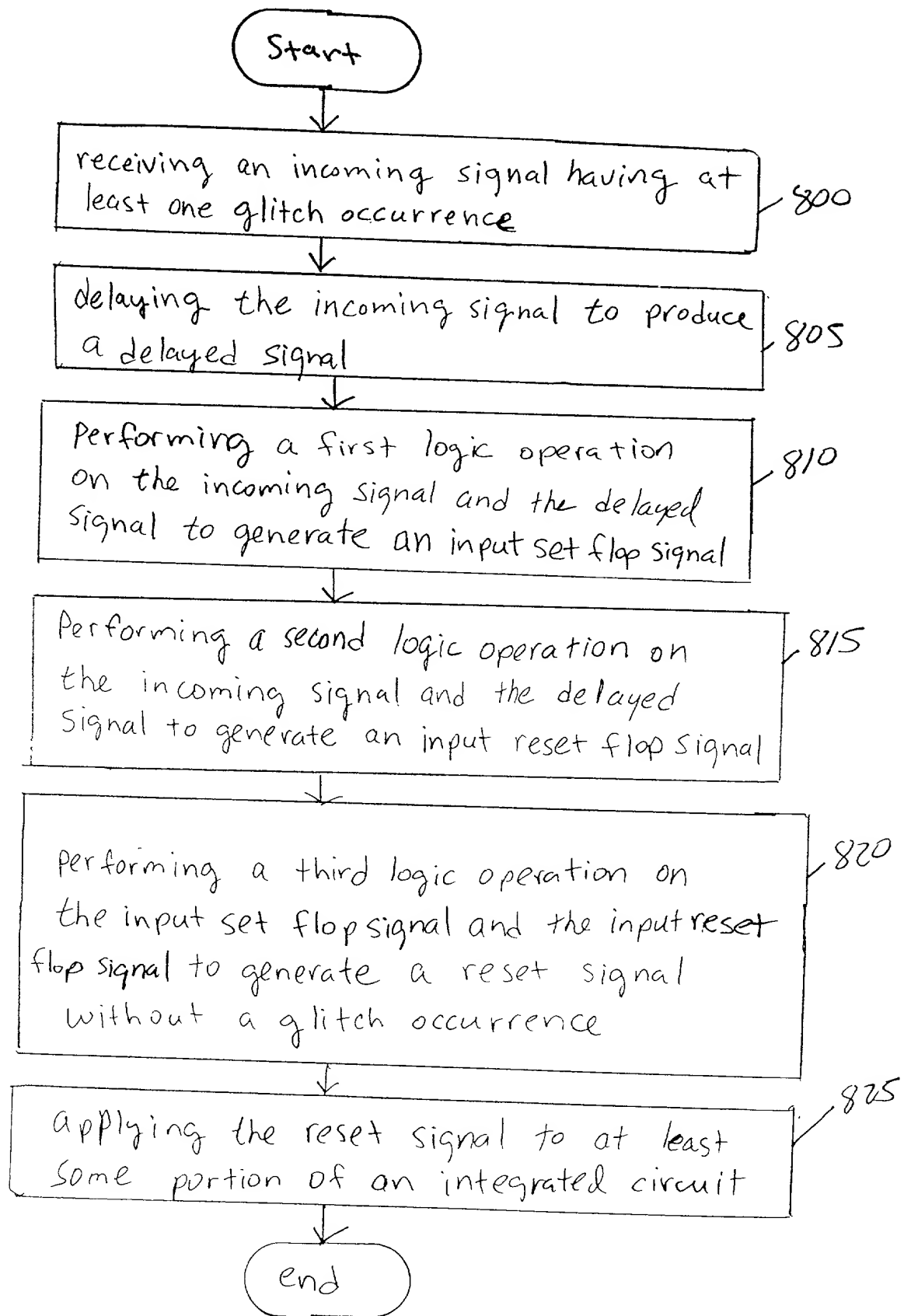


Figure 7



Method of compensating for glitch occurrence

Figure 8

Sample - Hold circuit

325b

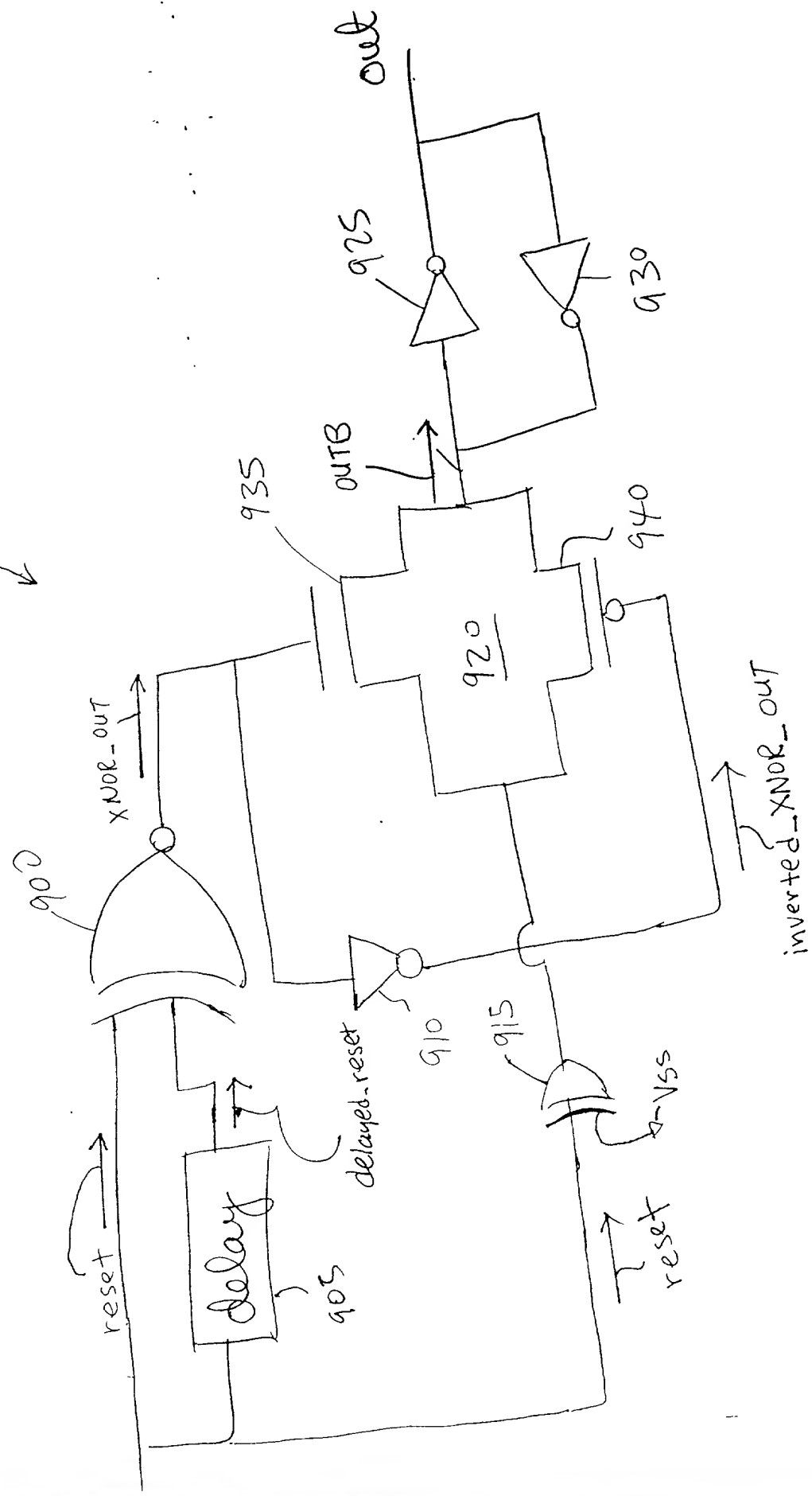


Figure 9

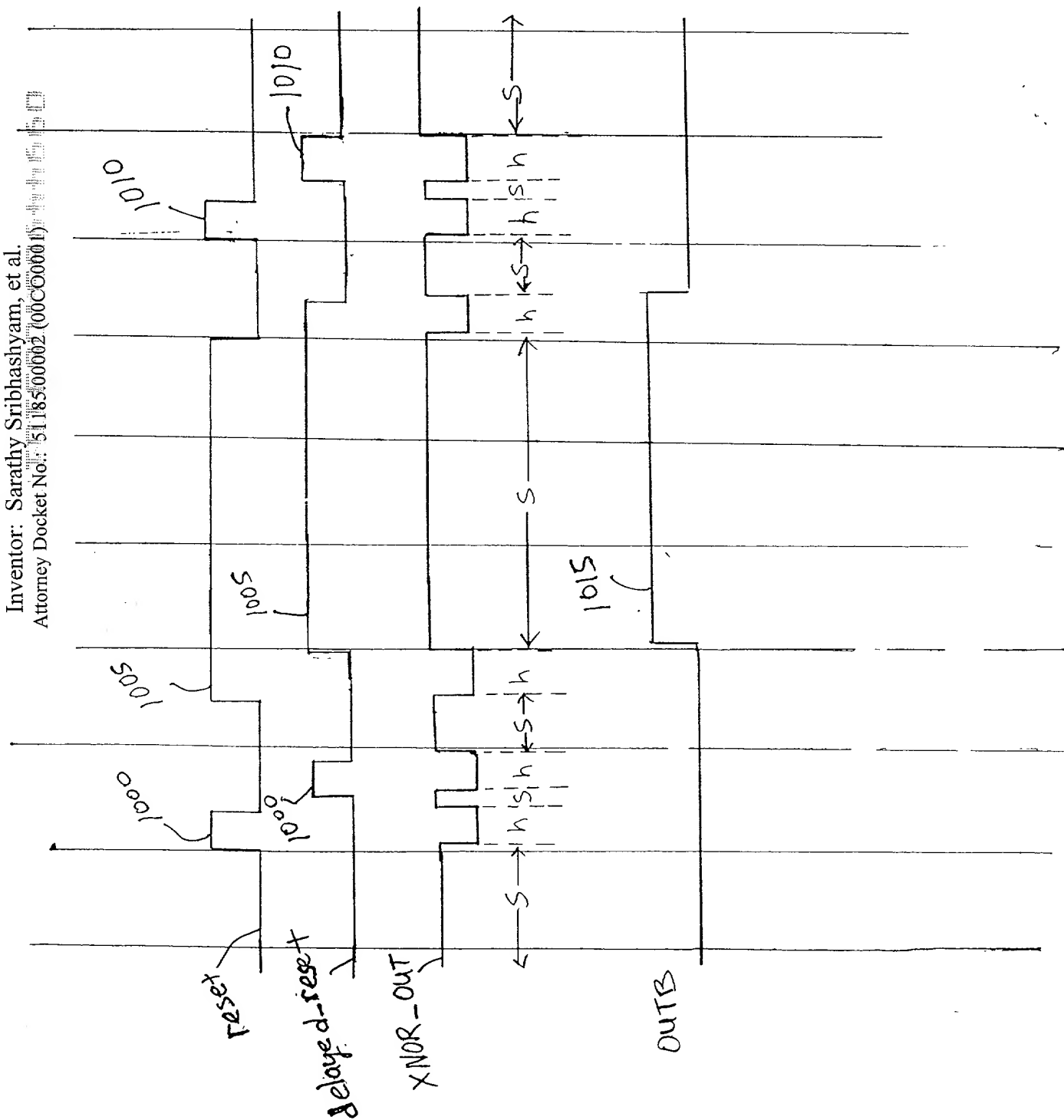


Figure 10

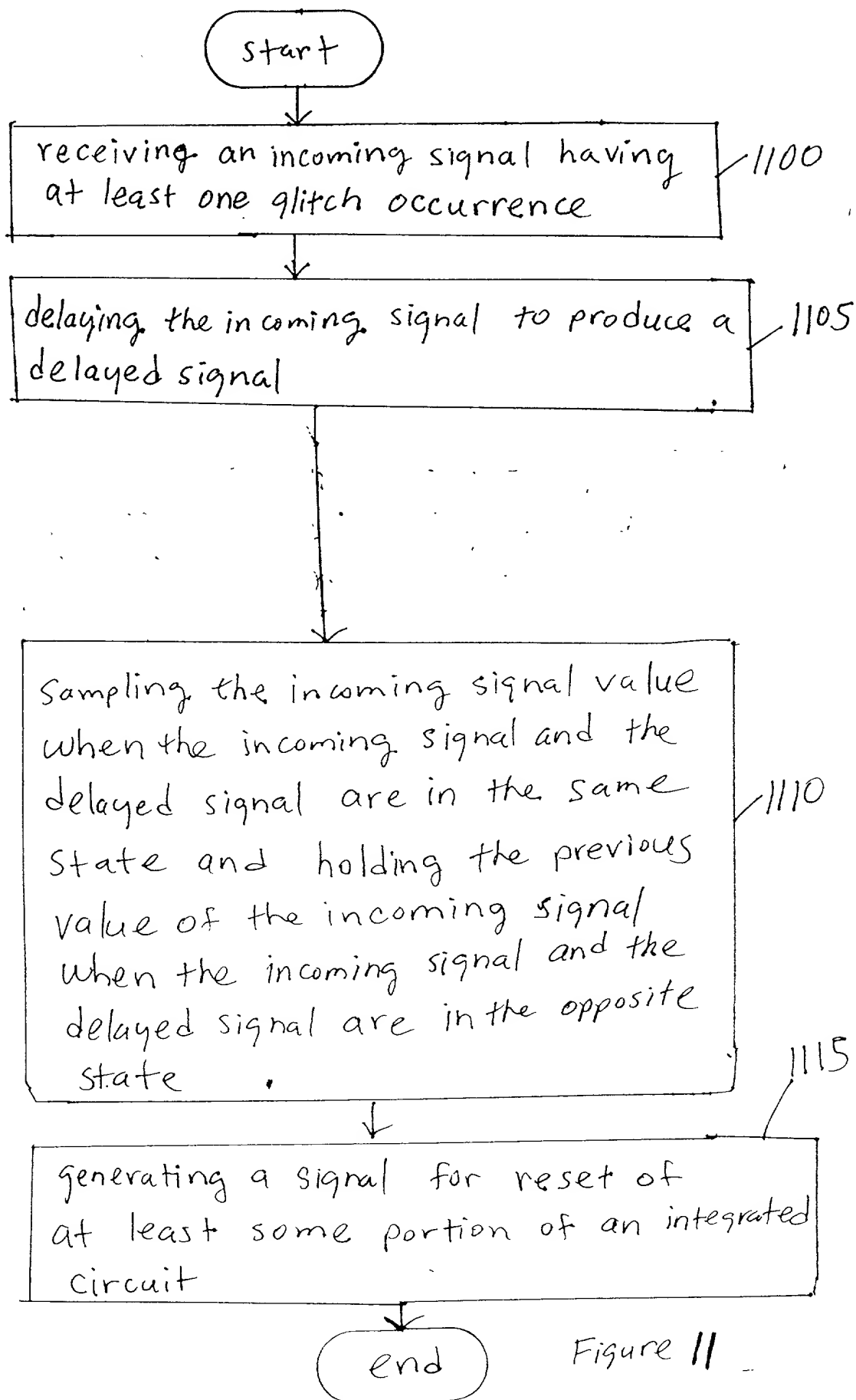


Figure 11